

FIG. 1 is a block diagram of a system 100. The system 100 includes a medium interface 102, a signal transmitter 104, a signal detector 106, a signal processor 116, a central controller 118, a user interface 120, a clock signal generator 110, a divider 112, and a random number generator 114. The clock signal generator 110 and the divider 112 are grouped together in a dashed box 108. The random number generator 114 is in another dashed box 114. The signal transmitter 104 and the signal detector 106 are connected to the medium interface 102. The signal detector 106 is connected to the signal processor 116. The signal processor 116 is connected to the central controller 118. The central controller 118 is connected to the user interface 120. The central controller 118 is also connected to the clock signal generator 110, the divider 112, and the random number generator 114.

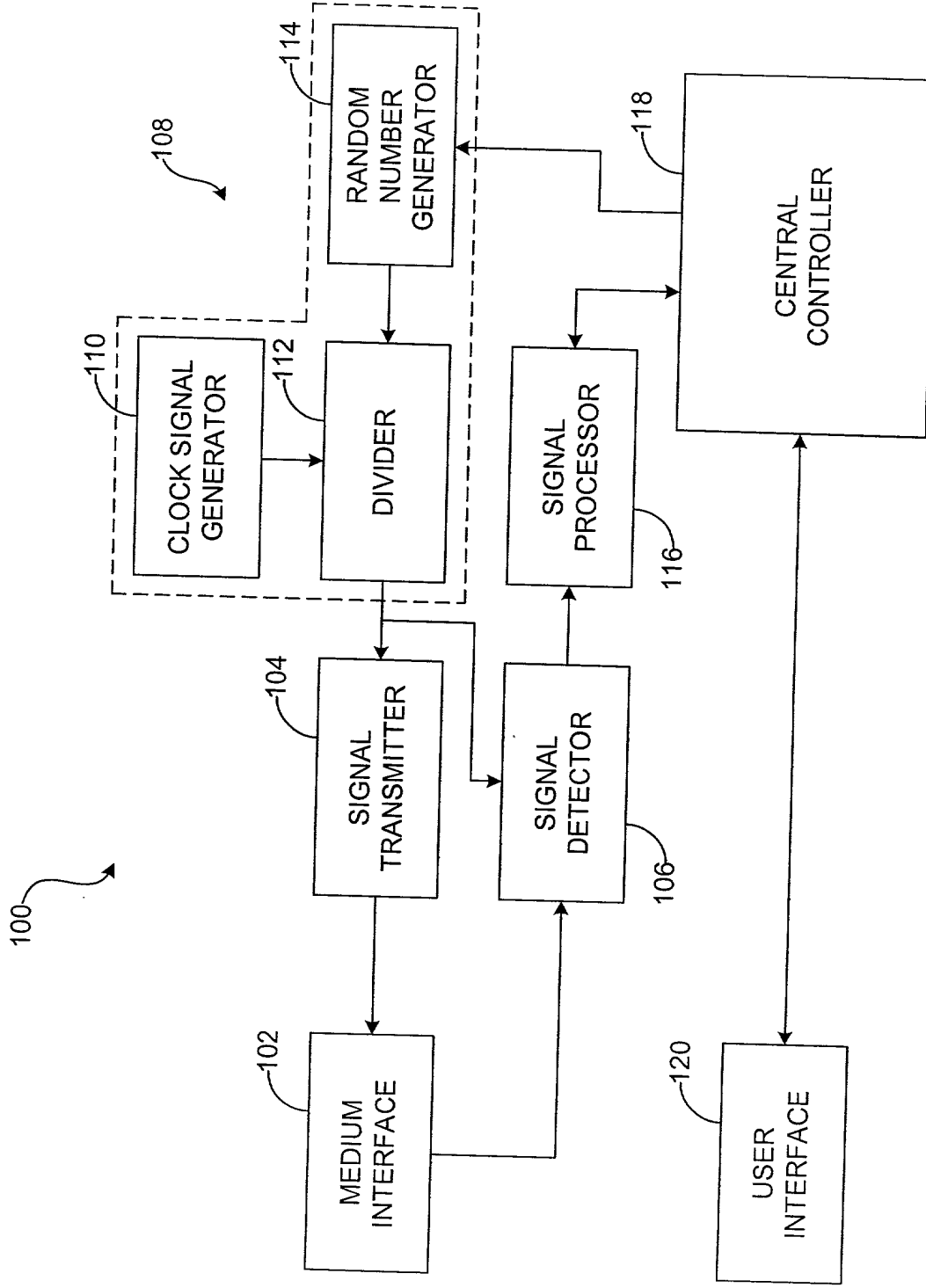
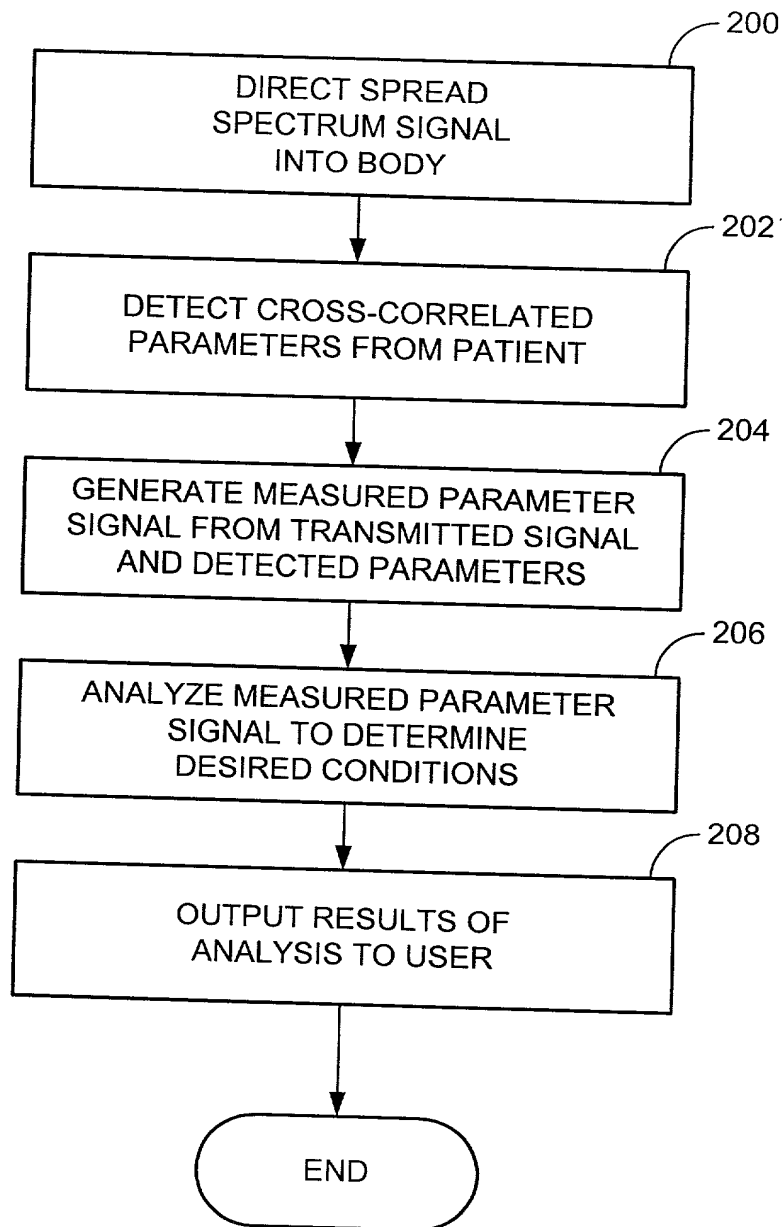
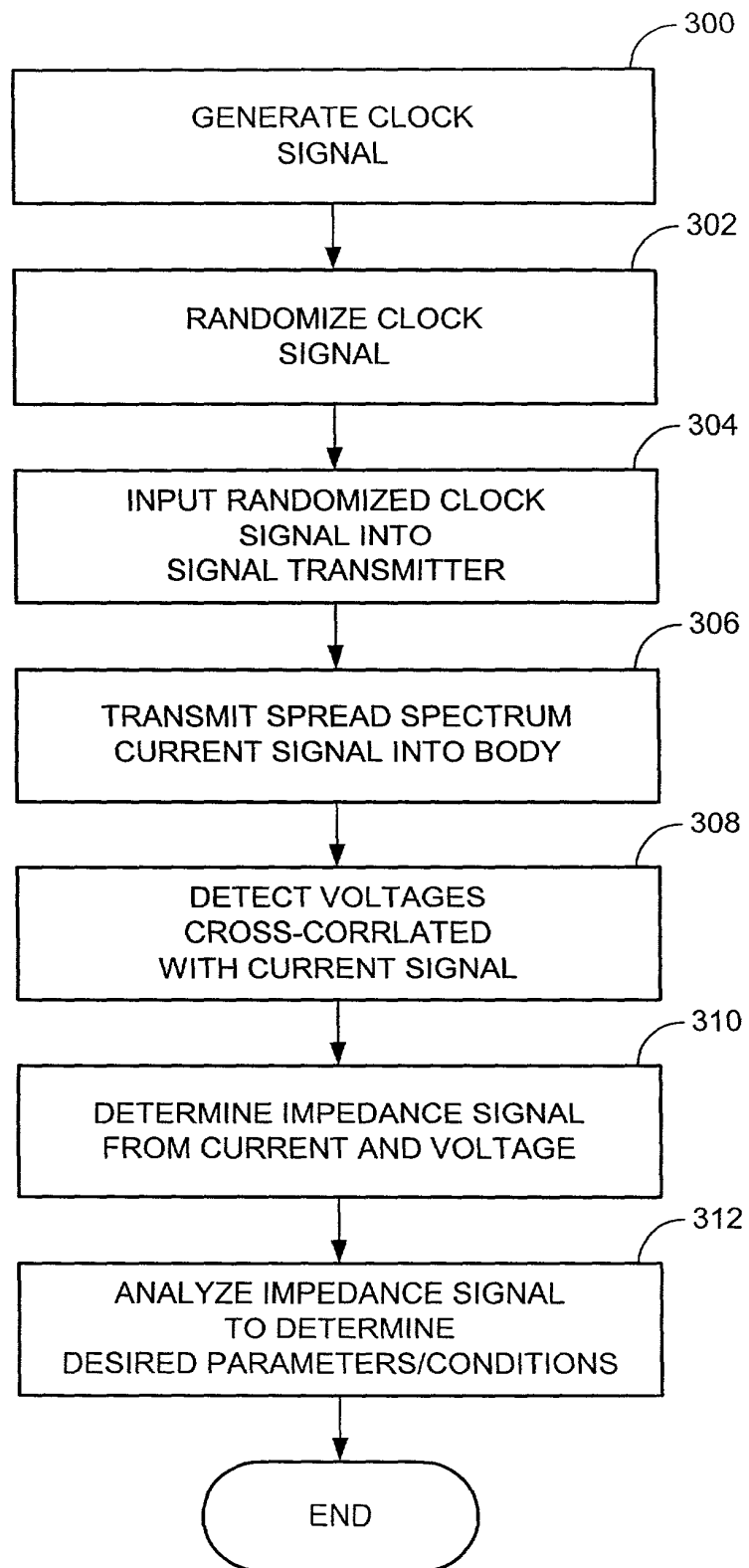


FIG. 1



**FIG. 2**



**FIG. 3**